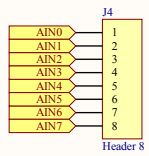
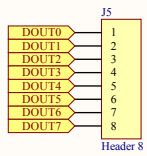


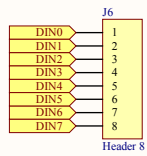
Analog IN



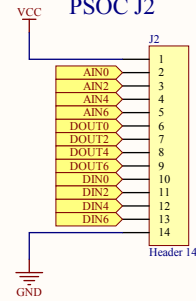
Digital IN



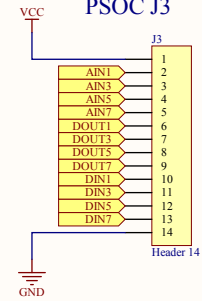
Digital Out



PSOC J2



PSOC J3



POSC Interface Board  
Tom Lafleur [lafleur@lafleur.us](mailto:lafleur@lafleur.us)

Title		
Size	Number	Revision
B		
Date:	1/13/2010	Sheet of
File:	C:\Altium Projects\POSC Board SchDoc	Drawn By: